**METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY**

B.E III-I (V SEMISTER), **1st INTERNAL EXAMINATIONS**, AUGUST 2018

Branch: **III** **ECE** **A&B** Subject: **COA**  Max Marks: **5**

Date of Examination: **31/8/2018** Paper: **Quiz** Time: **11AM to 11:10AM.**

1. How many bits we need to specify an address, for a memory unit with 4096 words [ ]
2. 16 b) 4 c) 8 d) 12
3. OPCODE for Register Reference Instructons [ ]

a)100 b) 110 c) 111 d) none

3. Size of the Decoder used in Control unit of a computer [ ]

 a)3x8m b)4x16 c)Both a&b d)5x32

4. Specify the type of Instruction when D**7**=0 [ ]

 a)Memory Reference b)Register Reference c)Input **/**output d)none

5. Logic Gate used in Add and Substract Algorithm to compare Sign Bits [ ]

 a ) OR b) XOR c) AND d)NAND

6.\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_is a group of bits that instruct computer to perform a specific operation.

7. Data register holds\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_.

8. A computer that employs a Micro Programmed control unit will have two separate memories \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ and \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_.

9. The Next Address Generator sometimes called \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_.

10. \_\_\_\_\_\_\_ bits are used to represent Address in the Instruction code.